

BIO

Dan Hammerstrom was a Program Manager at DARPA from March 2012 to March 2016. He came to DARPA from Portland State University, where he is a Professor in the Electrical and Computer Engineering (ECE) department.

From 1977 to 1980, Dr. Hammerstrom was an Assistant Professor in the Electrical Engineering Department at Cornell University. In 1980 he joined Intel in Oregon, where he was involved in computer architecture and VLSI design. In 1988 he founded Adaptive Solutions, Inc., which specialized in high performance silicon technology (the CNAPS chip set) for image processing, neural network emulation, and pattern recognition. In 1998 he joined the Oregon Graduate Institute, and then moved to Portland State in 2005.

Dr. Hammerstrom is a Life Fellow of the Institute of Electrical and Electronic Engineers (IEEE). He has been a Visiting Scientist at the Royal Institute of Technology in Stockholm, Sweden and the NASA Ames Research Center. He currently has a joint faculty appointment with Halmstad University, Halmstad Sweden. He received a PhD in Electrical Engineering from the University of Illinois at Urbana-Champaign in 1977.